

HARDWARE ERRATA REPORT

P/N **PCM-052**

phyCORE-Vybrid System on Module

Revision A0, 25 November 2013

Errata ID	Errata Title	Affected PCB No.
01	Cold Boot Reliability	1374.0
02	NAND boot failure	1374.0

E01: Cold Boot Reliability

Affected PCB: 1374.0 with silicon version 1.1

Errata Impact: The system will hang occasionally after DDR initialization on a cold boot.

Errata Description: During a cold boot, the DDR controller will use an invalid starting address, which causes the system to hang.

Workaround: Press the RESET button (button S2; label RST on the carrier board PCB) to issue a warm reset, and the system will then boot successfully.

Errata Fix: Linux PD13.1.0 BSP

E02: NAND Boot Failure

Affected PCB: 1374.0

Errata Impact: The Vybrid ROM loader cannot boot a u-boot image from the NAND device.

Errata Description: The NAND controller's $\text{NFC_R}/\overline{\text{B}}$ by default should be high, putting the NAND flash into a ready state. Instead, $\text{NFC_R}/\overline{\text{B}}$ signal floats at power on. The system reads this as busy, and therefore, not ready for commands.

Workaround: Solder re-work must be done on the phyCORE-Vybrid Carrier Board (PCM-952). Add a 4.7k Ω resistor between $\text{NFC_R}/\overline{\text{B}}$ and VDD_3V3. Figure 1 below, shows a 0402 package 4.7k Ω (1/16 W, 1-5%) resistor soldered to the top pad of R114 (not populated) for a connection to VDD_3V3, which is then connected by a wire to the $\text{NFC_R}/\overline{\text{B}}$ signal available at the respective via.

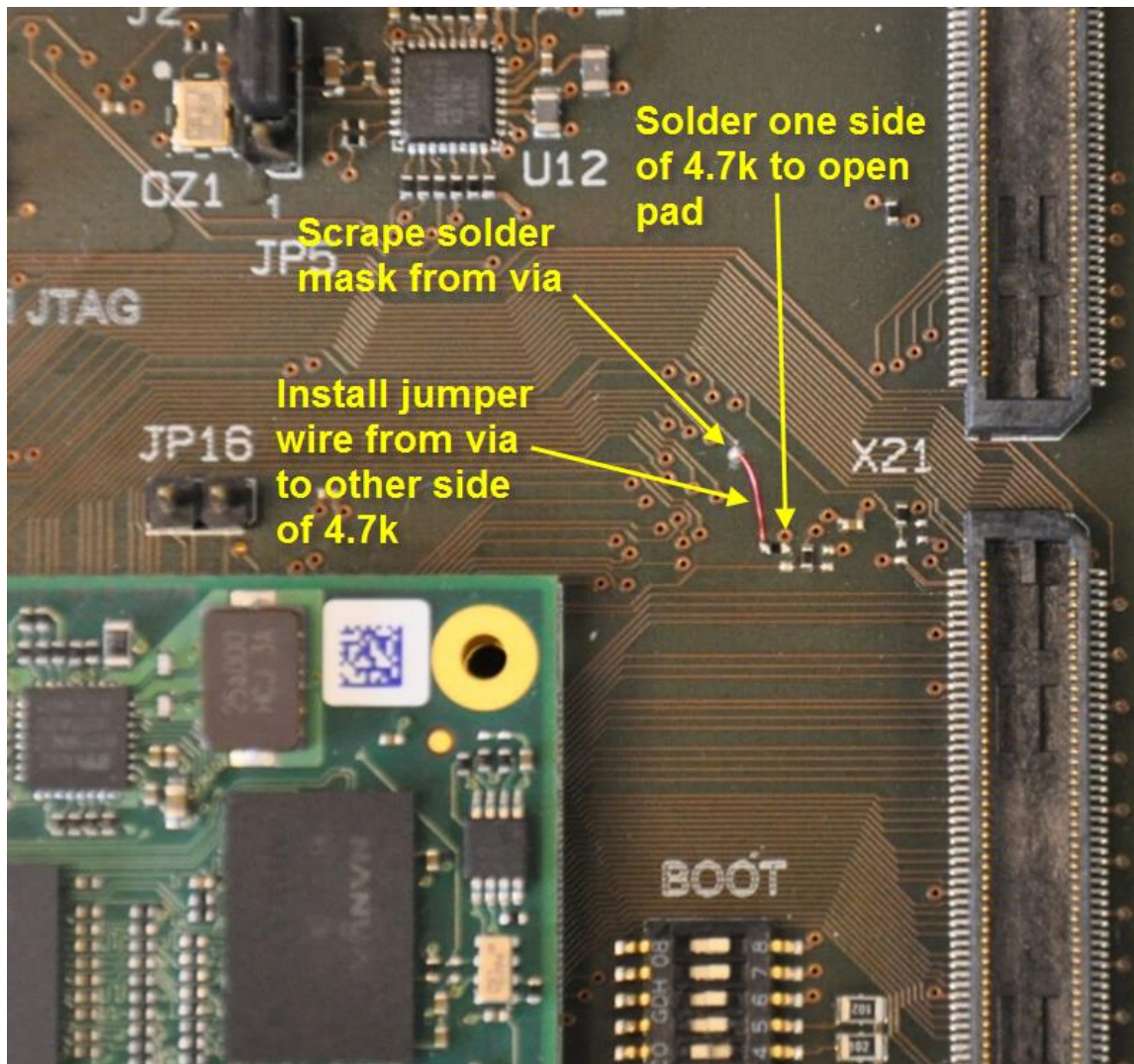


Figure 1: Errata 02 Rework

Errata Fix: 1374.1